

Application No. 10/612,364

Supplemental Amendment

*AMENDMENTS TO THE SPECIFICATION*

Replace the paragraph beginning at page 38, line 3 with:

At time T1, when input signal IN changes from 0 V to 1V, inverter 94 of control circuit 90 sets node N2 at 0 V. Accordingly, inverter INV1 turns on transistor ~~PT1~~ PT1 in response to the voltage level of node N2 to thereby electrically couple power supply voltage VDD to node Nb. That is, the voltage level of node Nb is set at 1 V. In addition, as the voltage level of node Nb is set at 1 V, inverter 93 of control circuit 90 turns off transistor 91 and turns on transistor 92. Therefore, nodes N2 and N0 are electrically coupled to each other. Accordingly, the supply of voltage to node N1 is stopped and transistor PT1 is turned off.